



1/10

10

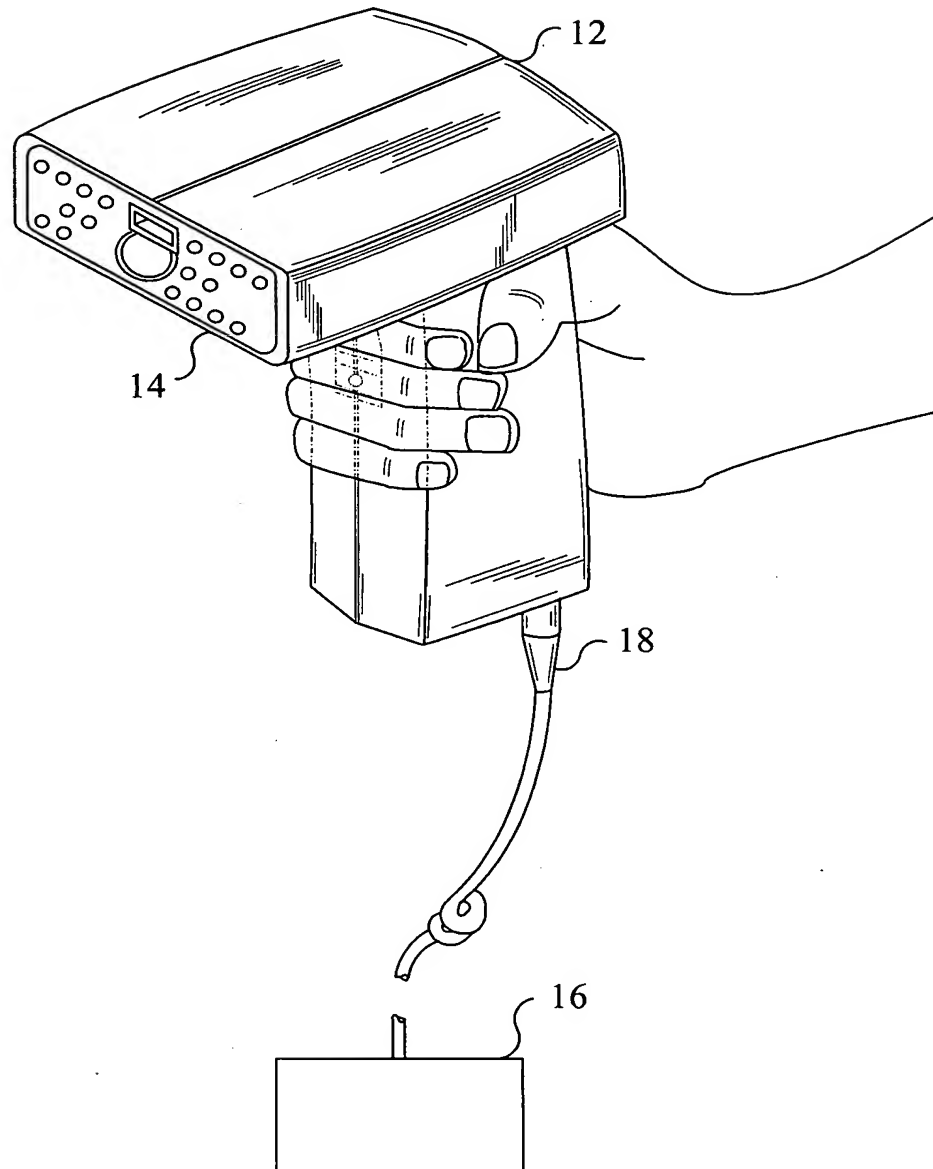


FIG. 1



2/10

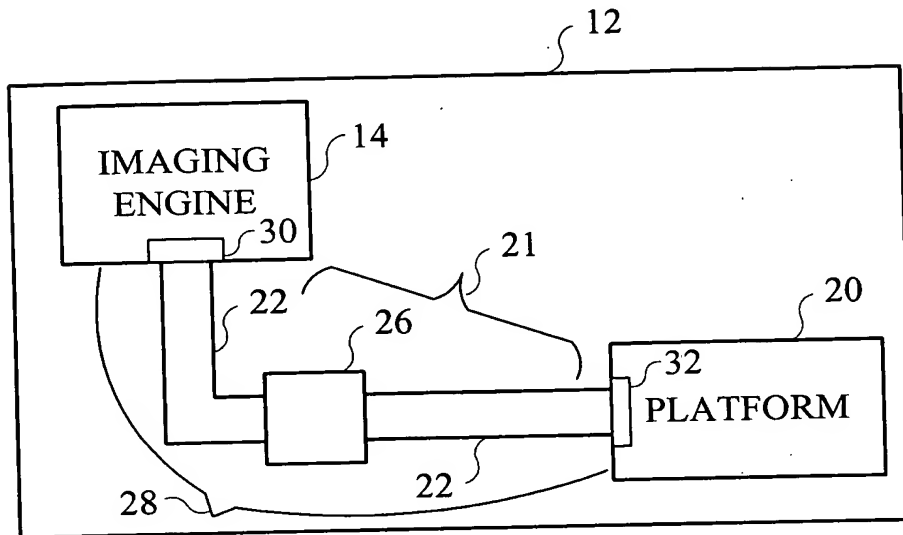


FIG. 2

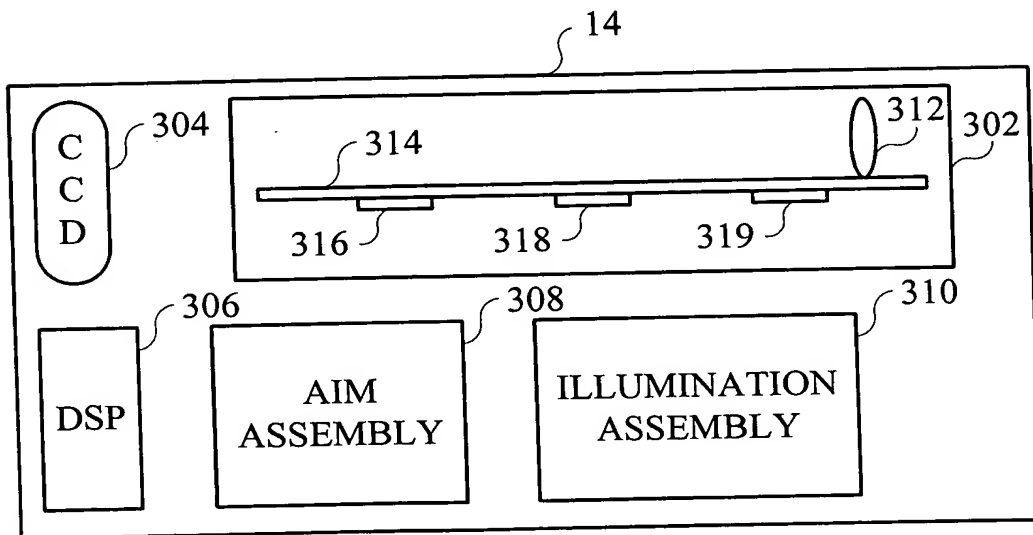


FIG. 3



3/10

PIN NUMBER	SIGNAL NAME	I/O	COMMENTS
1	GND	PWR	GROUND
2	PCLK	O	PIXEL CLOCK
3	GND	PWR	GROUND
4	HREF	O	HORIZONTAL PIXEL VALID CLOCK (SYNC)
5	VCC_MOTOR	PWR	FOCUS CONTROL MOTOR POWER
6	VCC_ILLUM	PWR	ILLUMINATION POWER
7	REG_RESET*	I/O	I2C REGISTER RESET
8	VCC	PWR	CCD/AIMING POWER
9	VCC	PWR	CCD/AIMING POWER
10	EXSFT	I	EXTERNAL FRAME SYNC.
11	ILLUM_ENB*	I	ILLUMINATION POWER
12	AIM_ENB*	I	AIM ENABLE
13	PIX_D0	O	PIXEL DATA BIT 0
14	PIX_D1	O	PIXEL DATA BIT 1
15	PIX_D2	O	PIXEL DATA BIT 2
16	PIX_D3	O	PIXEL DATA BIT 3
17	PIX_D4	O	PIXEL DATA BIT 4
18	PIX_D5	O	PIXEL DATA BIT 5
19	PIX_D6	O	PIXEL DATA BIT 6
20	PIX_D7	O	PIXEL DATA BIT 7
21	FOCUS_CTRL1	I	FOCUS CONTROL LINE 1
22	VREF	O	VERTICAL FRAME VALID CLOCK (SYNC)
23	EXHT	I	EXTERNAL HORIZONTAL CLOCK
24	I2C_SDA	I/O	I2C DATA
25	I2C_SCL	I	I2C CLOCK
26	GND	PWR	GROUND
27	MCKI	I	MASTER CLOCK
28	GND	PWR	GROUND
29	FOCUS_CTRL2	I	FOCUS CONTROL LINE 2
30	CS*	I	CHIP SELECT
31	FRAME_SHIFT	O	FRAME SHIFT AFTER EXPOSURE FOR MODE 1

FIG. 4

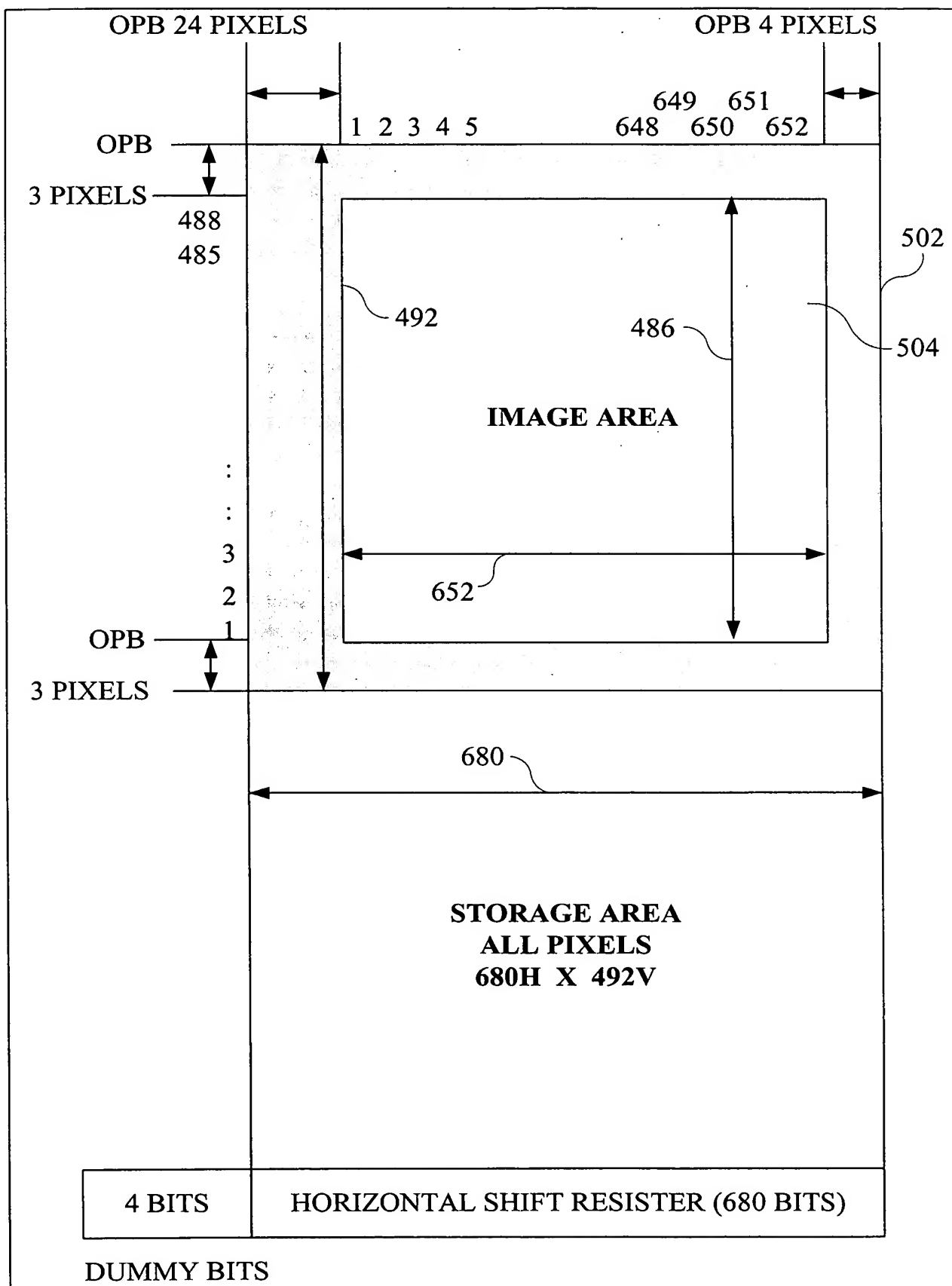


FIG. 5

5/10

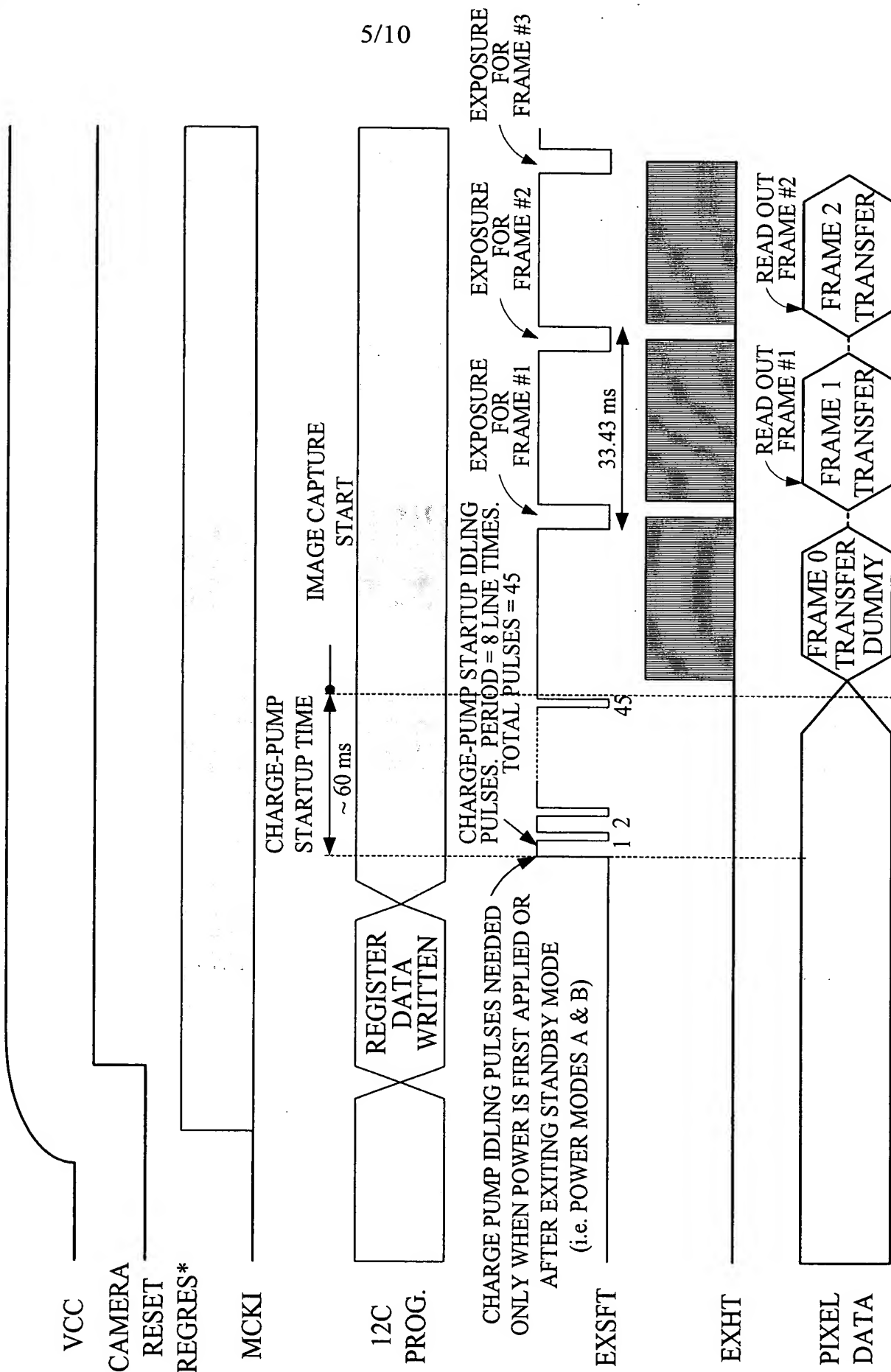


FIG. 6

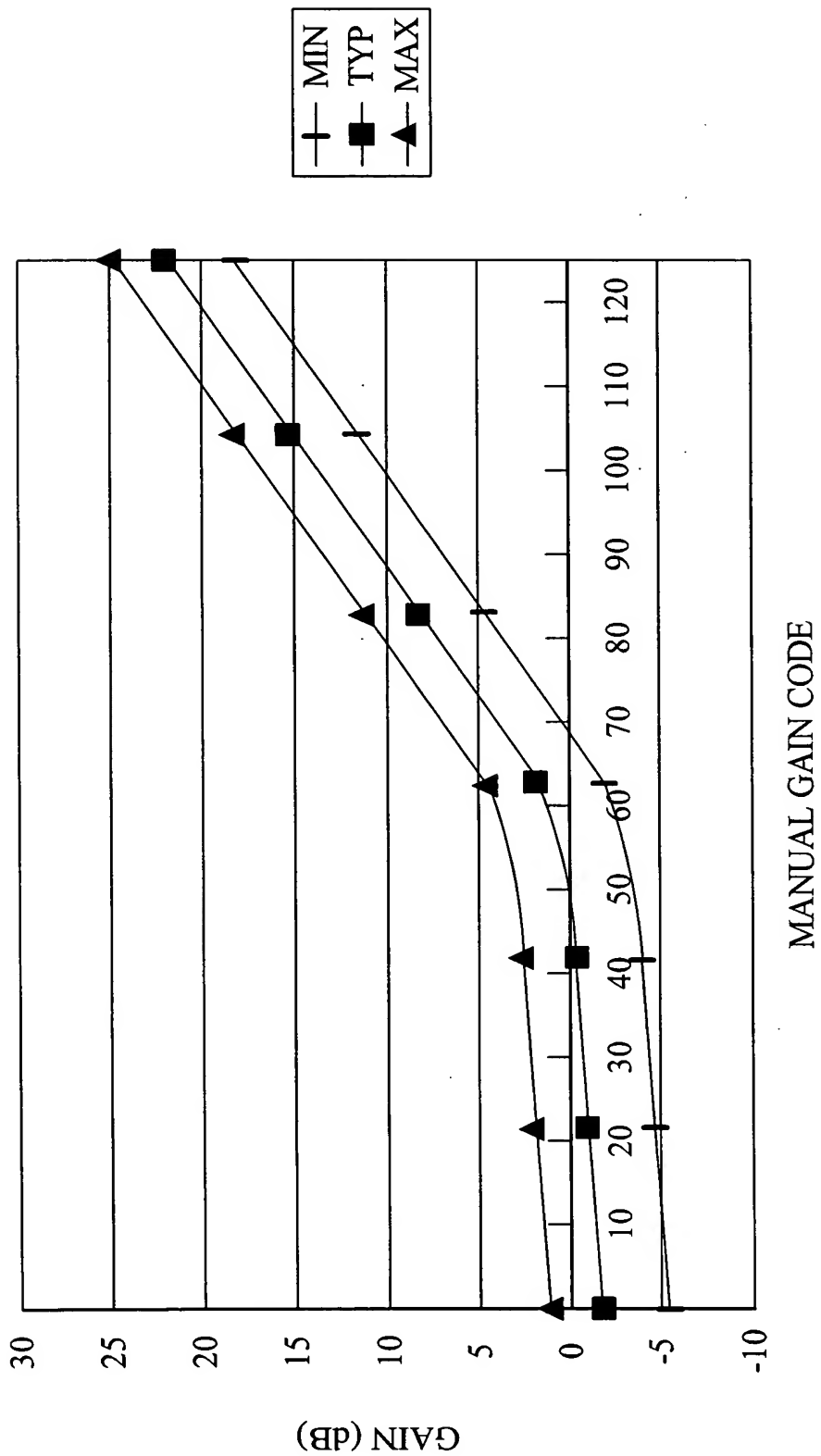


FIG. 7



7/10

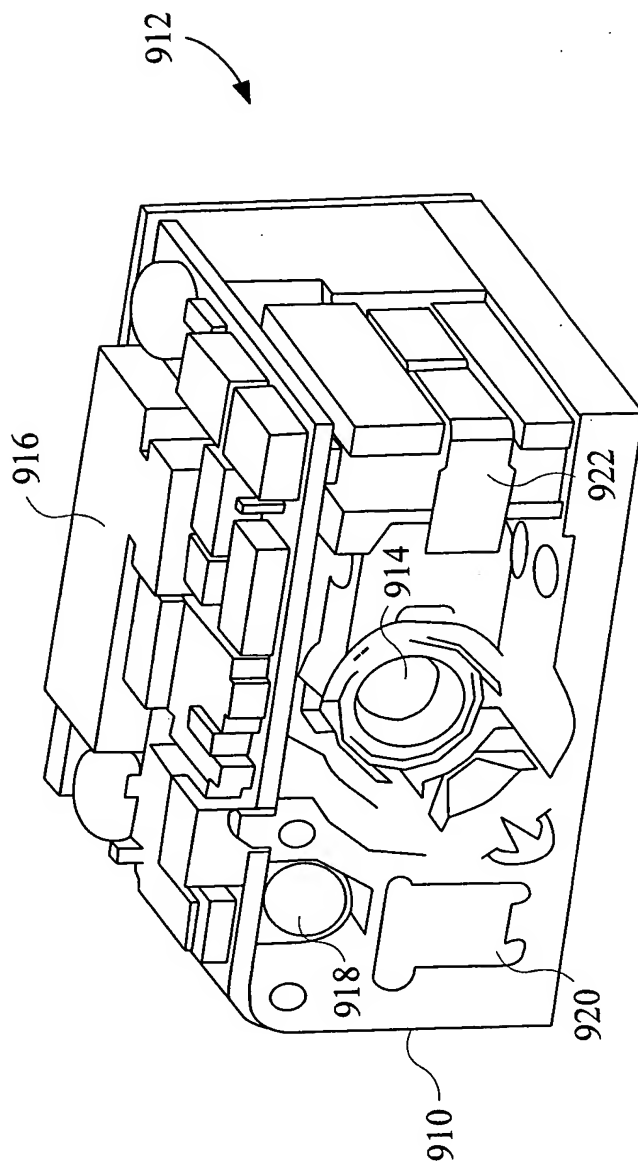


FIG. 8

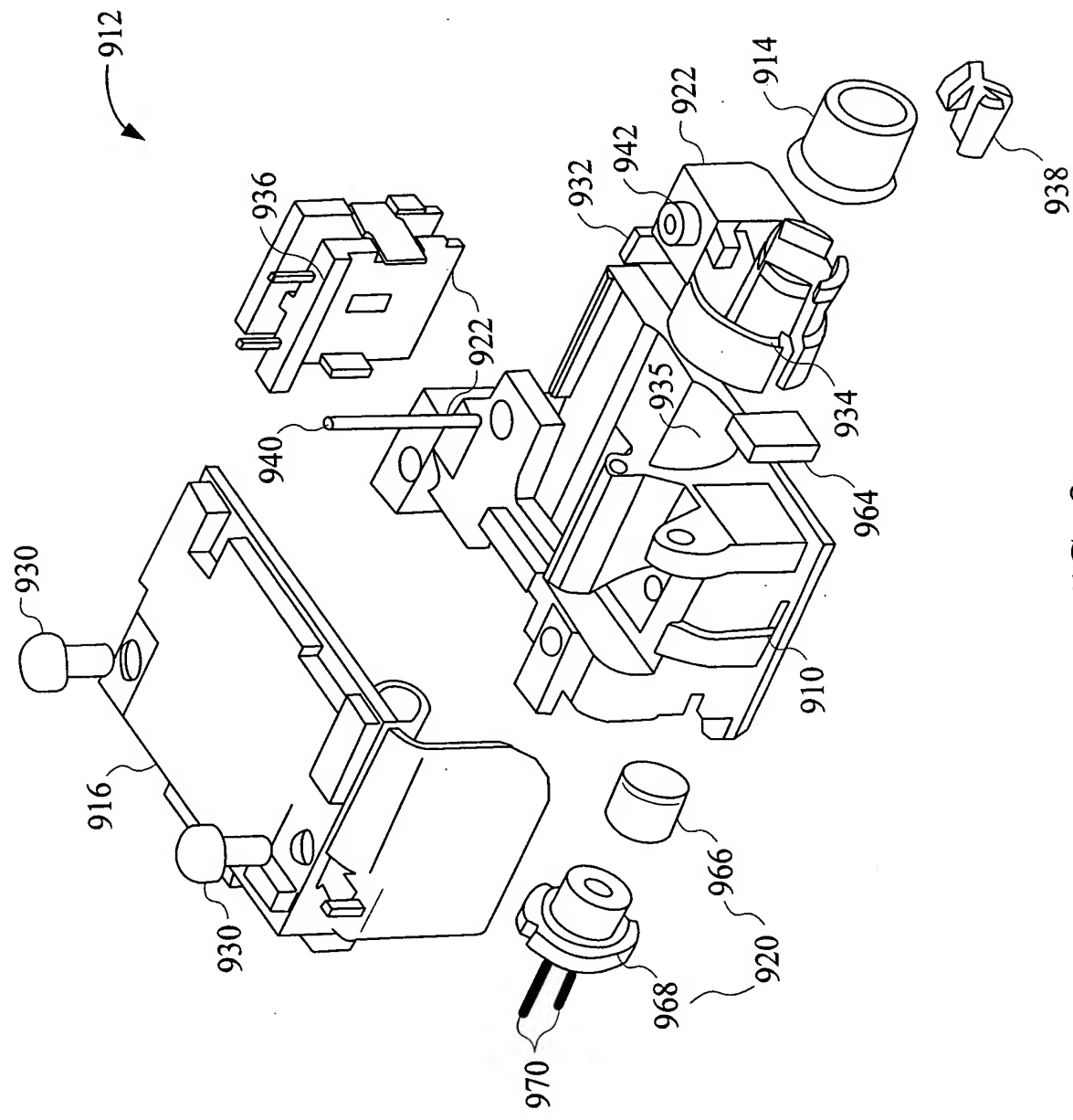


FIG. 9



9/10

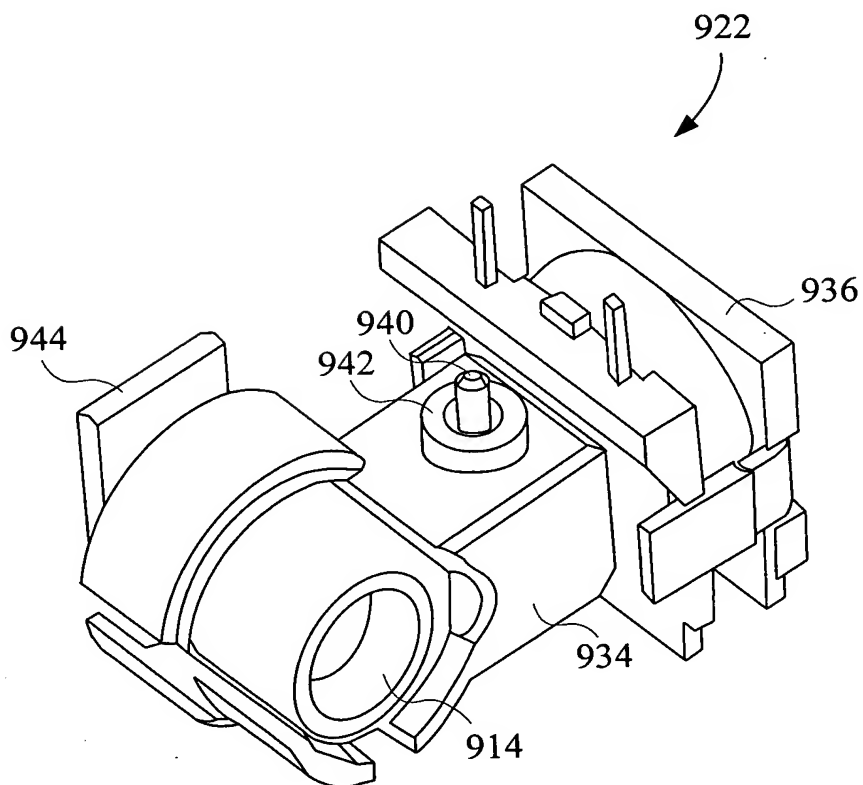


FIG. 10



10/10

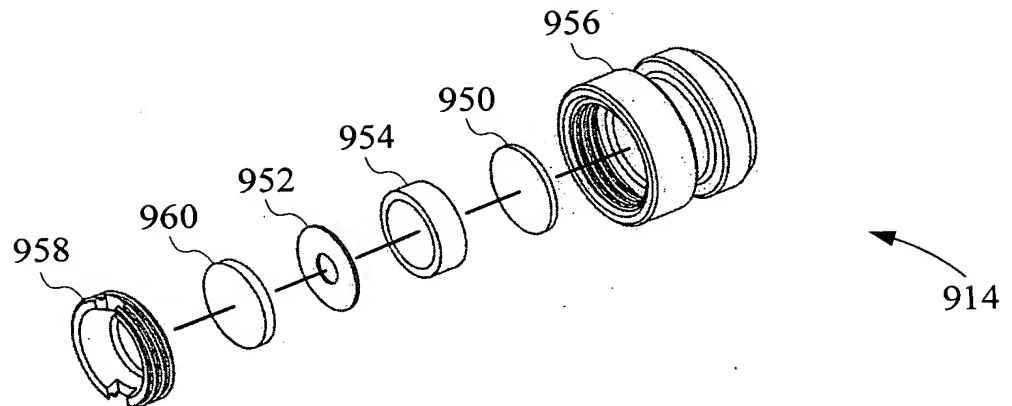


FIG. 11